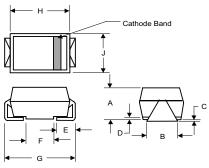


ER2A THRU ER2M

SURFACE MOUNT SUPER FAST RECTIFIER

Reverse Voltage - 50 to 1000 Volts Forward Current - 2.0 Amperes

DO-214AA (HSMB) (Round Lead)



DIMENSIONS										
	INCHES		MM							
DIM	MIN	MAX	MIN	MAX	NOTE					
Α	.078	.116	1.98	2.95						
В	.075	.089	1.90	2.25						
С	.002	.008	.05	.20						
D		.02		.51						
Е	.035	.055	.90	1.40						
F	.065	.091	1.65	2.32						
G	.205	.224	5.21	5.69						
Н	.160	.180	4.06	4.57						
	130	155	3 30	3.04						

FEATURES

- ◆ Lead Free Finish/Rohs Compliant (Note1) ("P"Suffix designates
- Compliant. See ordering information)
- * Case Material: Molded Plastic. UL Flammability
- ◆ Classification Rating 94 V-0 and MSL rating 1
- Easy Pick And Place
- ◆ High Temp Soldering: 260°C for 10 Seconds At Terminals
- Ultrafast Recovery Times For High Efficiency

MECHANICAL DATA

Case: JEDEC DO-214AA molded plastic body over passivated chip **Terminals**: Solder plated, solderable per MIL-STD-750,

Method 2026

Polarity: Color band denotes cathode end

Mounting Position: Any

Weight: 0.005 ounce, 0.138 grams

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified. Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.

TWGMC Catalog Number	SYMBOLS	ER2A	ER2B	ER2C	ER2D	ER2E	ER2G	ER2J	ER2K	ER2M	UNITS
Maximum repetitive peak reverse voltage	VRRM	50	100	150	200	300	400	600	800	1000	VOLTS
Maximum RMS voltage	VRMS	35	70	105	140	210	280	420	560	700	VOLTS
Maximum DC blocking voltage	VDC	50	100	150	200	300	400	600	800	1000	VOLTS
Maximum average forward rectified current at TL=55°C	l(AV)	2.0							Amps		
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	Ігѕм	50.0							Amps		
Maximum instantaneous forward voltage at 2.0A	VF		0.975 1.35				1.	Volts			
Maximum DC reverse current T _A =25℃ at rated DC blocking voltage T _A =100℃	lR	5.0 150.0							μΑ		
Maximum reverse recovery time (NOTE 1)	trr	50		60		100		ns			
Typical junction capacitance (NOTE 2)	Сл	25.0							pF		
Typical thermal resistance (NOTE 3)	RθJA	20.0							°C/W		
Operating junction and storage temperature range	ТЈ Тѕтс	-50 to +150							°C		

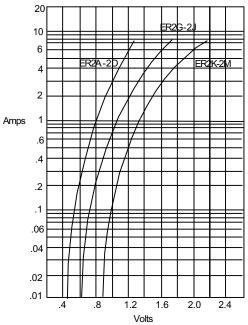
Note:1.Reverse recovery condition IF=0.5A,IR=1.0A,Irr=0.25A

- 2. Measured at 1MHz and applied reverse voltage of 4.0V D.C.
- 3. Pulse test: Pulse width 200 sec, Duty cycle 2%
- 4. High Temperature Solder Exemptions Applied, see EU Directive Annex 7.

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RATINGS AND CHARACTERISTIC CURVES ER2A THRU ER2M

Figure 1 Typical Forward Characteristics



Forward Derating Curve 2.4 2.2 2.0 1.8 1.6 1.4 1.2 Amps 1.0 .8 .6 Single Phase, Half Wave .2 60Hz Resistive or Inductive Load 0 25 50 75 100 125 150 ٥С

Figure 2

Average Forward Rectified Current - Amperes/ersus Ambient Temperature - $^{\circ}$ C

Instantaneous Forward Current - Amperes versus Instantaneous Forward Voltage - Volts

Figure 3 Junction Capacitance 100 60 40 20 T_J=225°d рF 10 6 4 2 .2 1 2 4 10 20 40 100 200 400 1000 Volts

Junction Capacitance - pF*versus* Reverse Voltage - Volts

RATINGS AND CHARACTERISTIC CURVES ER2A THRU ER2M

Figure 4
Peak Forward Surge Current

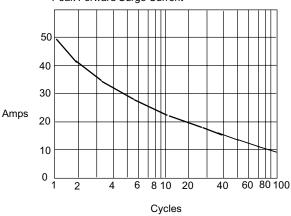
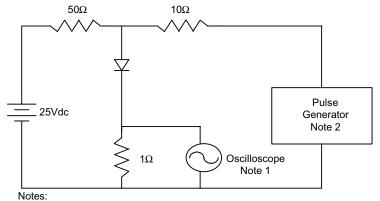


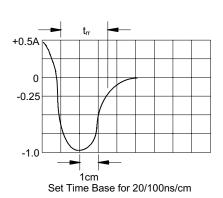
Figure 5
New SMB Assembly

Round Lead
Process

Peak Forward Surge Current - Amperesversus Number Of Cycles At 60Hz - Cycles

Figure 6
Reverse Recovery Time Characteristic And Test Circuit Diagram





1. Rise Time = 7ns max.

Input impedance = 1 megohm, 22pF

2. Rise Time = 10ns max.

Source impedance = 50 ohms

3. Resistors are non-inductive